



Memory Information and Procedures

Product: 6641A, 6642A, 6643A, 6644A, 6645A Power Supplies (includes all options unless otherwise noted)

Date: August 10, 2006

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|--|---------------------------|
| Memory Type: CMOS EEPROM | Memory Size: 2K |
| Memory Function: GPIB address, model, cal constants, 5 store states | |
| User Modifiable (Y/N): Yes | Volatile (Y/N): No |
| Memory Erase Processes: Store zero in locations 0 thru 5 | |

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|--|---------------------------|
| Memory Type: CMOS ROM | Memory Size: 256K |
| Memory Function: Operating instructions, languages and model parameters | |
| User Modifiable (Y/N): No | Volatile (Y/N): No |
| Memory Erase Processes: Not user modifiable - OTP device | |

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|--|---------------------------|
| Memory Type: CMOS MPU w/o ROM | Memory Size: |
| Memory Function: | |
| User Modifiable (Y/N): No | Volatile (Y/N): No |
| Memory Erase Processes: Not user modifiable No memory | |

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|---|---------------------------|
| Memory Type: CMOS RAM | Memory Size: 64K |
| Memory Function: Operating instructions, voltage, current parameters | |
| User Modifiable (Y/N): No | Volatile (Y/N): No |
| Memory Erase Processes: Cycle AC power | |

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|---|---------------------------|
| Memory Type: CMOS MPU with ROM | Memory Size: 4Kx8 |
| Memory Function: Operating instructions, voltage, current parameters | |
| User Modifiable (Y/N): No | Volatile (Y/N): No |
| Memory Erase Processes: Not user modifiable - masked device | |

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|--------------------------------------|---------------------------|
| Memory Type: CMOS MPU w/o ROM | Memory Size: |
| Memory Function: | |
| User Modifiable (Y/N): No | Volatile (Y/N): No |
| Memory Erase Processes: | |

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